

NSS40301MDR2G

Dual Matched 40 V, 6.0 A, Low $V_{CE(sat)}$ NPN Transistor

These transistors are part of the ON Semiconductor e²PowerEdge family of Low $V_{CE(sat)}$ transistors. They are assembled to create a pair of devices highly matched in all parameters, including ultra low saturation voltage $V_{CE(sat)}$, high current gain and Base/Emitter turn on voltage.

Typical applications are current mirrors, differential amplifiers, DC-DC converters and power management in portable and battery powered products such as cellular and cordless phones, PDAs, computers, printers, digital cameras and MP3 players. Other applications are low voltage motor controls in mass storage products such as disc drives and tape drives. In the automotive industry they can be used in air bag deployment and in the instrument cluster. The high current gain allows e²PowerEdge devices to be driven directly from PMU's control outputs, and the Linear Gain (Beta) makes them ideal components in analog amplifiers.

Features

- Current Gain Matching to 10%
- Base Emitter Voltage Matched to 2 mV
- This is a Pb-Free Device

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

| Rating | Symbol | Max | Unit |
|--------------------------------|-----------|----------------------------|------|
| Collector-Emitter Voltage | V_{CEO} | 40 | Vdc |
| Collector-Base Voltage | V_{CBO} | 40 | Vdc |
| Emitter-Base Voltage | V_{EBO} | 6.0 | Vdc |
| Collector Current - Continuous | I_C | 3.0 | A |
| Collector Current - Peak | I_{CM} | 6.0 | A |
| Electrostatic Discharge | ESD | HBM Class 3B MM Class C | |

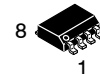
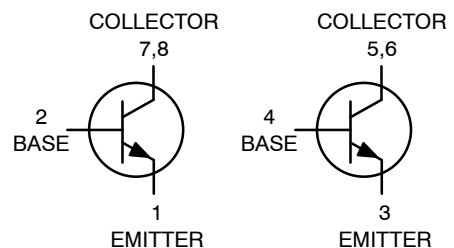
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



ON Semiconductor®

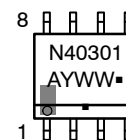
<http://onsemi.com>

40 VOLTS
6.0 AMPS
NPN LOW $V_{CE(sat)}$ TRANSISTOR
EQUIVALENT $R_{DS(on)}$ 44 mΩ



SOIC-8
CASE 751
STYLE 16

DEVICE MARKING



N40301 = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
▪ = Pb-Free Package
(Note: Microdot may be in either location)

ORDERING INFORMATION

| Device | Package | Shipping† |
|---------------|---------------------|-----------------------|
| NSS40301MDR2G | SOIC-8 (Pb-Free) | 2500 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

NSS40301MDR2G

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|----------------|--------|-----|------|
|----------------|--------|-----|------|

SINGLE HEATED

| | | | |
|--|-----------------|-----|----------------------------|
| Total Device Dissipation (Note 1) $T_A = 25^\circ\text{C}$ Derate above 25°C | P_D | 576 | mW |
| | | 4.6 | $\text{mW}/^\circ\text{C}$ |
| Thermal Resistance, Junction-to-Ambient (Note 1) | $R_{\theta JA}$ | 217 | $^\circ\text{C}/\text{W}$ |
| Total Device Dissipation (Note 2) $T_A = 25^\circ\text{C}$ Derate above 25°C | P_D | 676 | mW |
| | | 5.4 | $\text{mW}/^\circ\text{C}$ |
| Thermal Resistance, Junction-to-Ambient (Note 2) | $R_{\theta JA}$ | 185 | $^\circ\text{C}/\text{W}$ |

DUAL HEATED (Note 3)

| | | | |
|--|-----------------|-------------|----------------------------|
| Total Device Dissipation (Note 1) $T_A = 25^\circ\text{C}$ Derate above 25°C | P_D | 653 | mW |
| | | 5.2 | $\text{mW}/^\circ\text{C}$ |
| Thermal Resistance, Junction-to-Ambient (Note 1) | $R_{\theta JA}$ | 191 | $^\circ\text{C}/\text{W}$ |
| Total Device Dissipation (Note 2) $T_A = 25^\circ\text{C}$ Derate above 25°C | P_D | 783 | mW |
| | | 6.3 | $\text{mW}/^\circ\text{C}$ |
| Thermal Resistance, Junction-to-Ambient (Note 2) | $R_{\theta JA}$ | 160 | $^\circ\text{C}/\text{W}$ |
| Junction and Storage Temperature Range | T_J, T_{stg} | -55 to +150 | $^\circ\text{C}$ |

1. FR-4 @ 10 mm², 1 oz. copper traces, still air.
2. FR-4 @ 100 mm², 1 oz. copper traces, still air.
3. Dual heated values assume total power is the sum of two equally powered devices.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

| Characteristic | Symbol | Min | Typ | Max | Unit |
|----------------|--------|-----|-----|-----|------|
|----------------|--------|-----|-----|-----|------|

OFF CHARACTERISTICS

| | | | | | |
|---|----------------------|-----|---|-----|------|
| Collector–Emitter Breakdown Voltage (I _C = 10 mAdc, I _B = 0) | V _{(BR)CEO} | 40 | – | – | Vdc |
| Collector–Base Breakdown Voltage (I _C = 0.1 mAdc, I _E = 0) | V _{(BR)CBO} | 40 | – | – | Vdc |
| Emitter–Base Breakdown Voltage (I _E = 0.1 mAdc, I _C = 0) | V _{(BR)EBO} | 6.0 | – | – | Vdc |
| Collector Cutoff Current (V _{CB} = 40 Vdc, I _E = 0) | I _{CBO} | – | – | 0.1 | μAdc |
| Emitter Cutoff Current (V _{EB} = 6.0 Vdc) | I _{EBO} | – | – | 0.1 | μAdc |

ON CHARACTERISTICS

| | | | | | |
|---|---|-------------------------------------|--------------------------------------|--------------------------------------|-------------|
| DC Current Gain (Note 4) (I _C = 10 mA, V _{CE} = 2.0 V) (I _C = 500 mA, V _{CE} = 2.0 V) (I _C = 1.0 A, V _{CE} = 2.0 V) (I _C = 2.0 A, V _{CE} = 2.0 V) (I _C = 2.0 A, V _{CE} = 2.0 V) (Note 5) | h _{FE} h _{FE(1)} /h _{FE(2)} | 200 200 180 180 0.9 | 400 350 340 320 0.99 | – – – – – | |
| Collector–Emitter Saturation Voltage (Note 4) (I _C = 0.1 A, I _B = 0.010 A) (I _C = 1.0 A, I _B = 0.100 A) (I _C = 1.0 A, I _B = 0.010 A) (I _C = 2.0 A, I _B = 0.200 A) | V _{CE(sat)} | – – – – | 0.008 0.044 0.080 0.082 | 0.011 0.060 0.115 0.115 | V |
| Base–Emitter Saturation Voltage (Note 4) (I _C = 1.0 A, I _B = 0.01 A) | V _{BE(sat)} | – | 0.780 | 0.900 | V |
| Base–Emitter Turn–on Voltage (Note 4) (I _C = 0.1 A, V _{CE} = 2.0 V) (I _C = 0.1 A, V _{CE} = 2.0 V) (Note 6) | V _{BE(on)} V _{BE(1)} – V _{BE(2)} | – – | 0.650 0.3 | 0.750 2.0 | V mV |
| Cutoff Frequency (I _C = 100 mA, V _{CE} = 5.0 V, f = 100 MHz) | f _T | 100 | – | – | MHz |
| Input Capacitance (V _{EB} = 0.5 V, f = 1.0 MHz) | C _{ibo} | – | 320 | 450 | pF |
| Output Capacitance (V _{CB} = 3.0 V, f = 1.0 MHz) | C _{obo} | – | 40 | 50 | pF |

SWITCHING CHARACTERISTICS

| | | | | | |
|--|----------------|---|---|-----|----|
| Delay (V _{CC} = 30 V, I _C = 750 mA, I _{B1} = 15 mA) | t _d | – | – | 100 | ns |
| Rise (V _{CC} = 30 V, I _C = 750 mA, I _{B1} = 15 mA) | t _r | – | – | 100 | ns |
| Storage (V _{CC} = 30 V, I _C = 750 mA, I _{B1} = 15 mA) | t _s | – | – | 780 | ns |
| Fall (V _{CC} = 30 V, I _C = 750 mA, I _{B1} = 15 mA) | t _f | – | – | 110 | ns |

4. Pulsed Condition: Pulse Width = 300 μsec, Duty Cycle ≤ 2%.

5. h_{FE(1)}/h_{FE(2)} is the ratio of one transistor compared to the other transistor within the same package. The smaller h_{FE} is used as numerator.

6. V_{BE(1)} – V_{BE(2)} is the absolute difference of one transistor compared to the other transistor within the same package.

TYPICAL CHARACTERISTICS

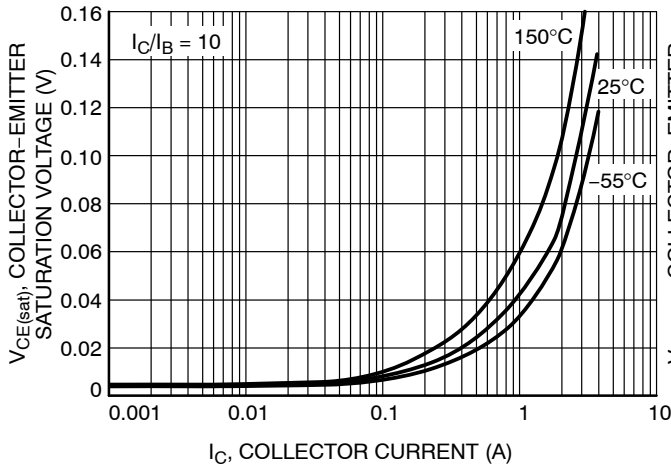


Figure 1. Collector Emitter Saturation Voltage vs. Collector Current

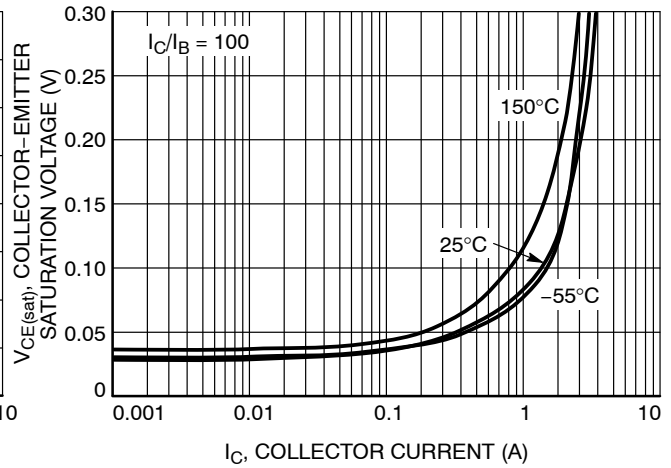


Figure 2. Collector Emitter Saturation Voltage vs. Collector Current

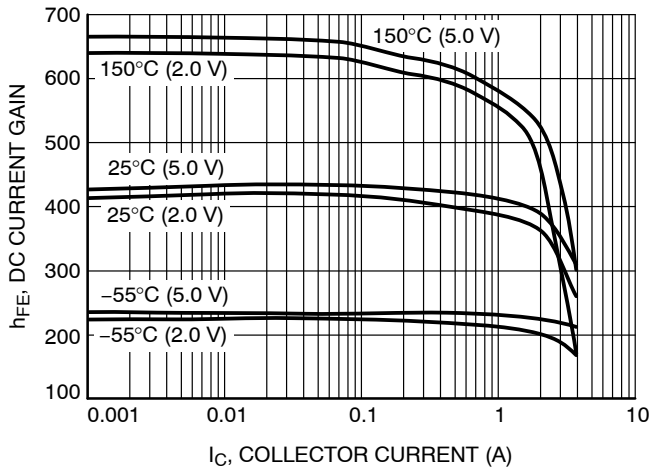


Figure 3. DC Current Gain vs. Collector Current

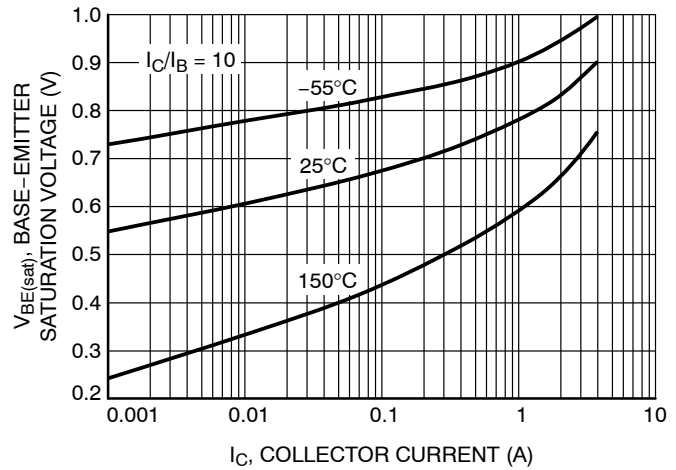


Figure 4. Base Emitter Saturation Voltage vs. Collector Current

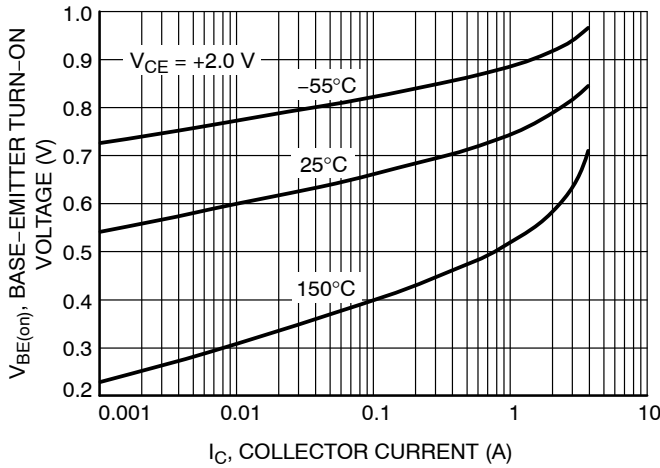


Figure 5. Base Emitter Turn-On Voltage vs. Collector Current

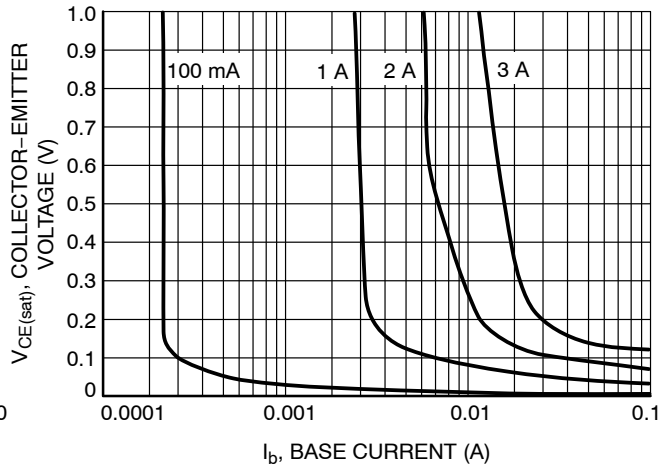


Figure 6. Saturation Region

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TYPICAL CHARACTERISTICS

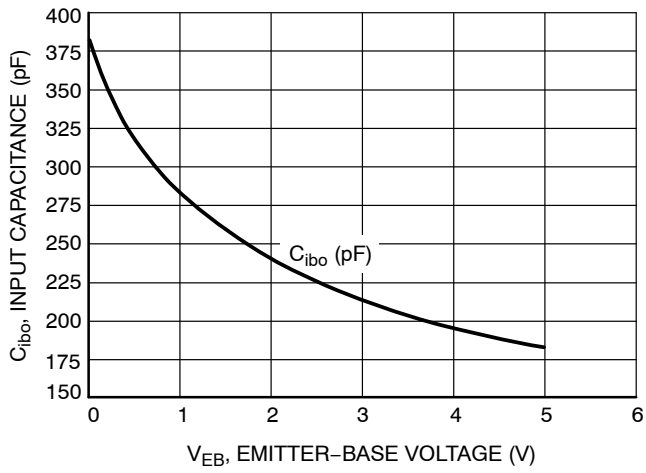


Figure 7. Input Capacitance

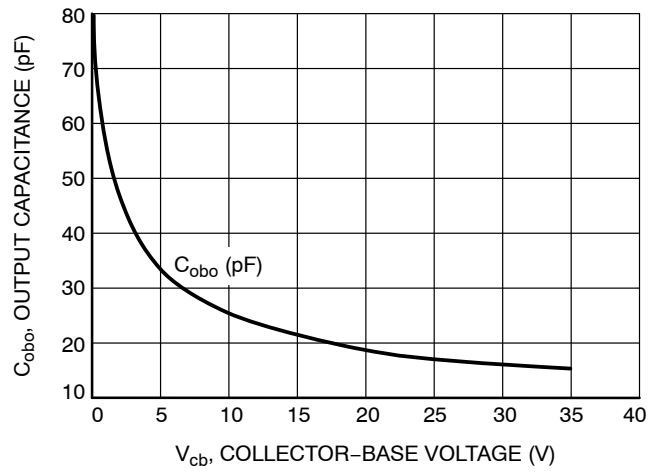


Figure 8. Output Capacitance

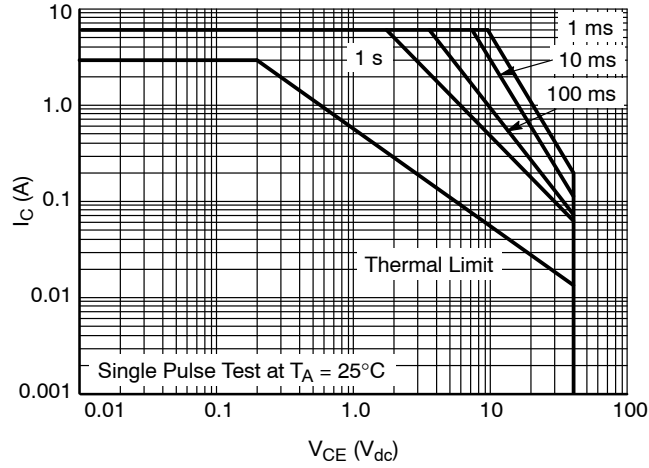
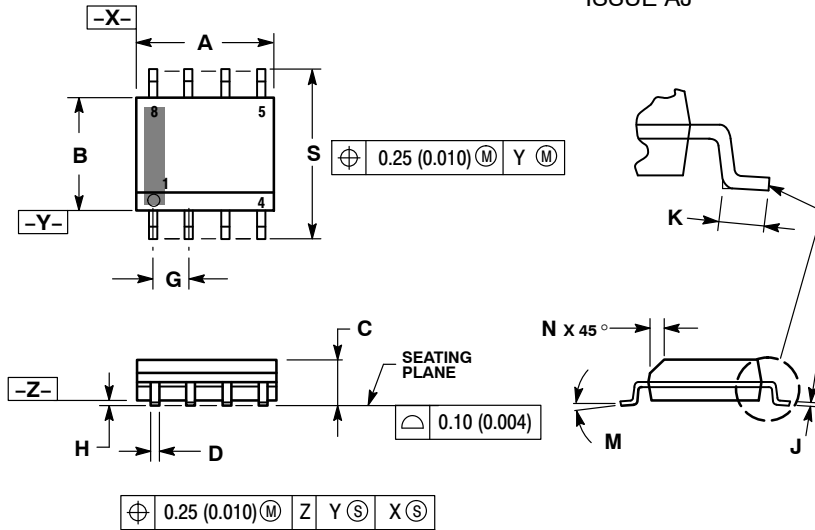


Figure 9. Safe Operating Area

NSS40301MDR2G

PACKAGE DIMENSIONS

SOIC-8 NB
CASE 751-07
ISSUE AJ

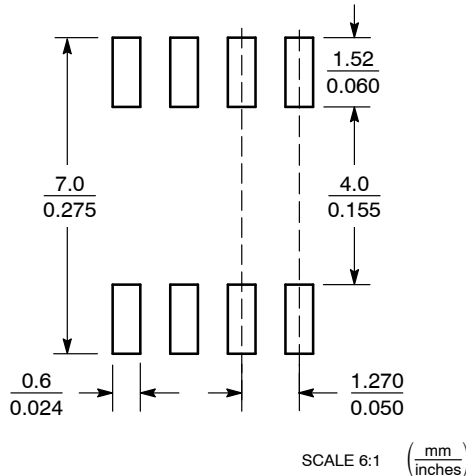


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.
6. 751-01 THRU 751-06 ARE OBSOLETE. NEW STANDARD IS 751-07.

| DIM | MILLIMETERS | | INCHES | |
|-----|-------------|------|-----------|-------|
| | MIN | MAX | MIN | MAX |
| A | 4.80 | 5.00 | 0.189 | 0.197 |
| B | 3.80 | 4.00 | 0.150 | 0.157 |
| C | 1.35 | 1.75 | 0.053 | 0.069 |
| D | 0.33 | 0.51 | 0.013 | 0.020 |
| G | 1.27 BSC | | 0.050 BSC | |
| H | 0.10 | 0.25 | 0.004 | 0.010 |
| J | 0.19 | 0.25 | 0.007 | 0.010 |
| K | 0.40 | 1.27 | 0.016 | 0.050 |
| M | 0° | 8° | 0° | 8° |
| N | 0.25 | 0.50 | 0.010 | 0.020 |
| S | 5.80 | 6.20 | 0.228 | 0.244 |

SOLDERING FOOTPRINT*



STYLE 16:

- PIN 1. EMITTER, DIE #1
- BASE, DIE #1
- EMITTER, DIE #2
- BASE, DIE #2
- COLLECTOR, DIE #2
- COLLECTOR, DIE #2
- COLLECTOR, DIE #1
- COLLECTOR, DIE #1

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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